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PTO-1449 (Modified)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. RA001C13	SERIAL NUMBER 10/037,171
	APPLICANT(S) Farunwald et al.	
	FILING DATE December 21, 2001	GROUP ART UNIT 2181

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	5,287,532	Feb. 15, 1994	Hunt			
	5,157,776	Oct. 20, 1992	Foster			
	5,023,838	Jun. 11, 1991	Herbert			
	4,961,171	Oct. 2, 1990	Pinkham et al.			
	4,930,065	May 22, 1990	McLagan et al.			
	4,782,439	Nov. 1, 1988	Borkar et al.			
	4,747,081	May 24, 1988	Heilveil et al.			
	4,704,678	Nov. 3, 1987	May			
	4,644,469	Feb. 17, 1987	Sumi			
	4,641,276	Feb. 3, 1987	Dunki-Jacobs			
	4,639,890	Jan. 27, 1987	Heilveil et al.			
	4,468,733	Aug. 28, 1984	Oka et al.			
	4,426,685	Jan. 17, 1984	Lorentzen			
	4,408,272	Oct. 4, 1983	Walters			
	4,257,097	Mar. 17, 1981	Moran			
	3,846,763	Nov. 5, 1974	Riikonen			
	H696	Oct. 3, 1989	Davidson			

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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	120563/1987	Jun. 1, 1987	Japan			
	0 189 576	Aug. 6, 1986	EPO			
	0 187 289	Jul. 16, 1986	EPO			
	0 166 192	Jan. 2, 1986	EPO			
	0 126 976	Dec. 5, 1984	EPO			
	135684/1984	Aug. 3, 1984	Japan			
	82/02615	Aug. 5, 1982	PCT			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Hopper et al., <i>Multiple vs. Wide Shared Bus Multiprocessors</i> , Proceedings of the 16 <sup>th</sup> Annual International Symposium on Computer Architecture, Jerusalem, Israel, June 1989, IEEE Computer Society Press, 1989.
	Gehringer et al., <i>A Survey of Commercial Parallel Processors</i> , Computer Architecture News, Vol. 16, No. 4, Sept. 1988.
	Dubois et al. <i>Effects of Cache Coherency in Multiprocessors</i> , IEEE Transaction in Computers, Vol. C31, No. 11, pgs. 1083-1099, November 1982.

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	5,847,997	Dec. 8, 1998	Harada et al.			
	5,148,523	Sep 15, 1992	Harlin et al.			
	5,142,637	Aug. 25, 1992	Harlin et al.			
	5,109,498	Apr. 28, 1992	Kamiya et al.			

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	61028248	Feb. 7, 1986	Japan			
	58184647	Oct. 28, 1983	Japan			

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	S. Muchmore, <i>Designing Computer Systems Based on Multibus II</i> , New Electronics, Vol. 20, No. 16, p. 31-32, Aug. 11, 1987.
	T.C. Poon et al., <i>A CMOS DRAM-Controller Chip Implementation</i> , IEEE Journal of Solid State Circuits, Vol. 22, No. 3, pp. 491-494, Jun. 1987.
	<i>Accordion Start-Stop Sequencer for a Variable Cycle Storage Controller</i> , IBM Technical Disclosure Bulletin, pp. 2074-2075, (a delphion.com reprint on two sheets), Oct. 1986.
	<i>Motorola 68030 Cache Organization</i> , posting to Internet Newsgroup net.arch by aglew@ccvaxa.UUCP, (a google.com reprint on two sheets), Sep. 29, 1986.
	Stanley D. Rosenbaum et al., <i>A 16 384-Bit High-Density CCD Memory</i> , IEEE Journal of Solid State Circuits, Vol. SC-11, No. 1, pp. 33-39, Feb. 1976

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	5,083,296	Jan. 21, 1992	Hara et al.			
	5,077,693	Dec. 31, 1991	Hardee et al.			
	4,937,734	Jun. 26, 1990	Bechtolsheim			
	4,920,483	Apr. 24, 1990	Pogue et al.			
	4,912,630	Mar. 27, 1990	Cochcroft, Jr.			
	4,807,189	Feb. 21, 1989	Pinkham et al.			
	4,799,199	Jan. 17, 1989	Scales, III et al.			
	4,788,667	Nov. 29, 1988	Nakano et al.			
	4,680,738	Jul. 14, 1987	Tam			
	4,675,850	Jun. 23, 1987	Kumanoya et al.			
	4,315,308	Feb. 9, 1982	Jackson			
	4,092,665	May 30, 1978	Saran			
	4,084,154	Apr. 11, 1978	Panigrahi			
	3,821,715	Jun. 28, 1974	Hoff, Jr. et al.			
	4,016,545	Apr. 5, 1977	Lipovski			

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